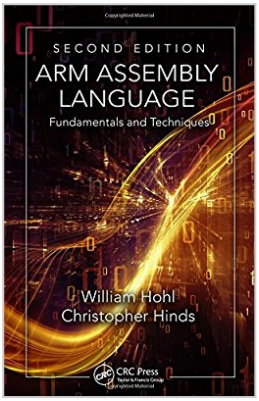

ARM Assembly Language: Fundamentals and Techniques, Second Edition

By William Hohl, Christopher Hinds

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Delivering a solid introduction to assembly language and embedded systems, **ARM Assembly Language: Fundamentals and Techniques, Second Edition** continues to support the popular ARM7TDMI, but also addresses the latest architectures from ARM, including Cortex™-A, Cortex-R, and Cortex-M processors?all of which have slightly different instruction sets, programmer's models, and exception handling.

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Editorial Review

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"Relaxed and informal, almost conversational, this writing style makes for comfortable reading that should appeal to everyone while breaking the tension of diving into the complexities of a modern multi-purpose microcontroller."
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?Rose M. Lowe, Clemson University, South Carolina, USA

About the Author
William Hohl is currently with Intel's Atom microprocessor group in Austin, Texas. He held the position of worldwide university relations manager for ARM, based in Austin, for 10 years. In total, he was with ARM for nearly 15 years and began as a principal design engineer to help build the ARM1020 microprocessor. In addition to his engineering duties, he also held an adjunct faculty position in Austin from 1998 to 2004, teaching undergraduate mathematics. Before joining ARM, he worked at Motorola (now Freescale Semiconductor) in the ColdFire and 68040 design groups and at Texas Instruments as an applications engineer. His travel and university lectures have taken him to over 40 countries on 5 continents. He holds MSEE and BSEE degrees from Texas A&M University as well as six patents in the field of debug architectures.

Christopher Hinds has worked in the microprocessor design field for over 25 years, holding design

positions at Motorola (now Freescale Semiconductor), AMD, and ARM. While at ARM, he was the primary author of the ARM VFP floating-point architecture and led the design of the ARM10 VFP, the first hardware implementation of the new architecture. He recently joined the Patents Group in ARM, identifying patentable inventions within the company and assisting in patent litigation. He holds BSEE and MSEE degrees from Texas A&M University and an M.Div from Oral Roberts University, where he worked to establish the School of Engineering, creating and teaching the first digital logic and microprocessor courses. He has numerous published papers and presentations on the floating-point architecture of ARM processors, and is a named inventor on over 30 US patents in the areas of floating-point implementation, instruction set design, and circuit design.

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